

1. A computer system:

a power supply;

a processor;

5 a memory storage device having a plurality of electrical input and output terminals, said electrical terminals including an control terminal for receiving control signals which control recording and transmission of information into and from said memory device;

an electrical harness for interconnecting said electrical terminals with said processor; and

10 a switch box including one or more manual switches, said switches including a control switch interconnected between said control terminal and said processor, said control switch having a first position allowing control signals to be transmitted from said processor to said memory storage device and a second position for preventing control signals to be transmitted from said processor to said memory storage device.

2. The computer system of claim 1 wherein said switch box includes a power switch interconnected between said power supply and said memory device, said power switch including a first position allowing power to be transmitted from said power supply to said memory device and a second position preventing power to be transmitted from said power supply to said memory device.

3. A computer system:

a power supply;

a processor;

5 a plurality of memory storage devices, said memory storage devices having a plurality of electrical input and output terminals, said electrical terminals of said memory storage devices including an control terminal for receiving control signals which control recording and transmission of information into and from said memory device;

10 an electrical harness for interconnecting said electrical terminals with said processor; and

15 a switch box including a plurality of manual switches, said switches including a plurality of control switches interconnected between said control terminals and said processor, said control switches having a first position allowing control signals to be transmitted from said processor to said memory storage devices and a second position for preventing control signals to be transmitted from said processor to said memory storage device.

4. The computer system of claim 3 wherein said switch box includes a plurality of power switches interconnected between said power supply and said memory storage devices, said power switches a first position allowing power to be transmitted from said power supply to said memory devices and a second position preventing power to be transmitted from said power supply to said memory devices.

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